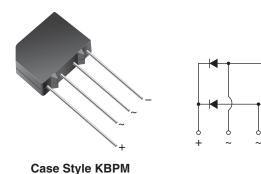


Vishay General Semiconductor

HALOGEN

FREE

Glass Passivated Single-Phase Bridge Rectifier



PRIMARY CHARACTERISTICS								
Package KBPM								
I _{F(AV)}	1.5 A							
V _{RRM}	50 V to 1000 V							
I _{FSM}	60 A							
I _R	5 μΑ							
V _F	1.0 V							
T _J max.	150 °C							
Diode variations	In-line							

FEATURES

- UL recognition file number E54214
- Ideal for printed circuit board
- High surge current capability
- · High case dielectric strength
- Solder dip 275 °C max. 10 s, per JESD 22-B106
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

TYPICAL APPLICATIONS

General purpose use in AC/DC bridge full wave rectification for switching power supply, home appliances, office equipment, and telecommunication applications.

MECHANICAL DATA

Case: KBPM

Molding compound meets UL 94 V-0 flammability rating Base P/N-M4 - halogen-free, RoHS-compliant, and commercial grade

Terminals: Silver plated leads, solderable per J-STD-002 and JESD 22-B102

Polarity: As marked on body

MAXIMUM RATINGS (T _A = 25 °C unless otherwise noted)									
DADAMETER	SYMBOL	KBP005M	KBP01M	KBP02M	KBP04M	KBP06M	KBP08M	KBP10M	UNIT
PARAMETER		3N246	3N247	3N248	3N249	3N250	3N251	3N252	
Maximum repetitive peak reverse voltage (1)	V_{RRM}	50	100	200	400	600	800	1000	V
Maximum RMS voltage (1)	V _{RMS}	35	70	140	280	420	560	700	V
Maximum DC blocking voltage (1)	V_{DC}	50	100	200	400	600	800	1000	V
Maximum average forward output rectified current at $T_A = 40 ^{\circ}\text{C}$	I _{F(AV)}	1.5							Α
Peak forward surge current T _A = 25 °C		60							Α
single half sine-wave $^{(1)}$ $T_A = 150 ^{\circ}C$	IFSM	40							
Rating for fusing (t < 8.3 ms)	g for fusing (t < 8.3 ms)		10						A ² s
Operating junction and storage temperature range $^{(1)}$ T_J , T_{ST}		-55 to +150							°C

Note

(1) JEDEC® registered values

ELECTRICAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)										
PARAMETER	TEST	CVARDOL	KBP005M	KBP01M	KBP02M	KBP04M	KBP06M	KBP08M	KBP10M	UNIT
PARAMETER	CONDITIONS	SYMBOL	3N246	3N247	3N248	3N249	3N250	3N251	3N252	UNIT
Maximum instantaneous	1.0 A		1.0							V
forward voltage drop per diode (1)	1.57 A	V_{F}	1.3							
Maximum DC reverse	T _J = 25 °C	_	5.0							
current at rated DC blocking voltage per diode (1)	T _J = 125 °C	I _R	500						μΑ	
Typical junction capacitance per diode	4.0 V, 1 MHz	CJ	15					рF		

Note

(1) JEDEC® registered values



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THERMAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)									
PARAMETER	SYMBOL	KBP005M	KBP005M KBP01M KBP02M KBP04M KBP06		KBP06M	KBP08M KBP10M		UNIT	
		3N246	3N247	3N248	3N249	3N250	3N251	3N252	UNIT
Typical thermal resistance (1)	$R_{\theta JA}$	40							°C/W
Typical thermal resistance (*)	$R_{ hetaJL}$	13						C/VV	

Note

⁽¹⁾ Thermal resistance from junction to ambient and from junction to lead mounted on PCB with, 0.47" x 0.47" (12 mm x 12 mm) copper pads

ORDERING INFORMATION (Example)									
PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE					
KBP06M-M4/51	1.895	51	600	Anti-static PVC tray					
3N250-M4/51	1.895	51	600	Anti-static PVC tray					

RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °C unless otherwise noted)

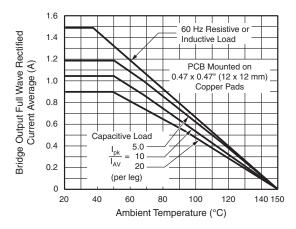


Fig. 1 - Derating Curve Output Rectified Current

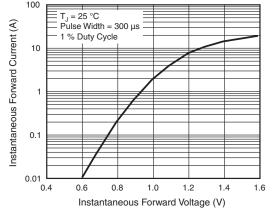


Fig. 3 - Typical Forward Characteristics Per Diode

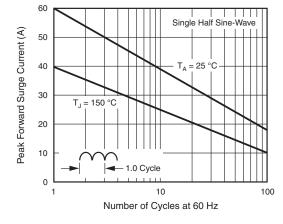


Fig. 2 - Maximum Non-Repetitive Peak Forward Surge Current Per Diode

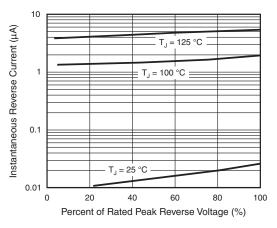


Fig. 4 - Typical Reverse Leakage Characteristics Per Diode



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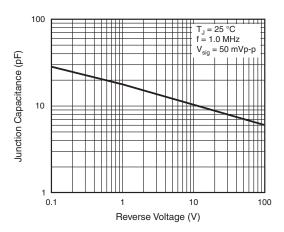
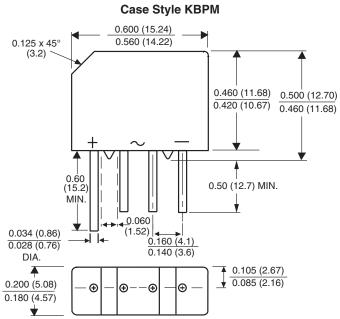


Fig. 5 - Typical Junction Capacitance Per Diode

PACKAGE OUTLINE DIMENSIONS in inches (millimeters)



Polarity shown on front side of case: positive lead by beveled corner



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