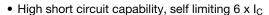


Molding Type Module IGBT, 2 in 1 Package, 1200 V and 400 A



PRODUCT SUMMARY						
V _{CES}	1200 V					
I _C at T _C = 80 °C	400 A					
$V_{CE(on)}$ (typical) at $I_C = 400 \text{ A}$, 25 °C	1.9 V					
Speed	8 kHz to 30 kHz					
Package	Double INT-A-PAK					
Circuit	Half bridge					

FEATURES





- 10 µs short circuit capability
- V_{CE(on)} with positive temperature coefficient
- Low inductance case
- · Fast and soft reverse recovery antiparallel FWD
- Isolated copper baseplate using DCB (Direct Copper Bonding) technology
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

TYPICAL APPLICATIONS

- · AC inverter drives
- Switching mode power supplies
- Eletronic welders

DESCRIPTION

Vishay's IGBT power module provides ultra low conduction loss as well as short circuit ruggedness. It is designed for applications such as general inverters and UPS.

ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C unless otherwise noted)					
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Collector to emitter voltage	V _{CES}		1200	V	
Gate to emitter voltage	V _{GES}		± 20	V	
Collector current at T 150 °C		T _C = 25 °C	800		
Collector current at T _J = 150 °C	Ic	T _C = 80 °C	400		
Pulsed collector current	I _{CM} ⁽¹⁾	t_p = 1 ms, T_C = 80 °C	800	А	
Diode continuous forward current	I _F	T _C = 80 °C	400		
Diode maximum forward current	I _{FM}	t _p = 1 ms	800		
Maximum power dissipation	P _D	T _J = 150 °C	2604	W	
Short circuit withstand time	t _{SC}	T _J = 125 °C	10	μѕ	
RMS isolation voltage	V _{ISOL}	f = 50 Hz, t = 1 min	2500	V	
l ² t-value, diode	l ² t	$V_R = 0 \text{ V}, \text{ t} = 10 \text{ ms}, T_J = 125 ^{\circ}\text{C}$	34	kA ² s	

Note

⁽¹⁾ Repetitive rating: pulse width limited by maximum junction temperature.



IGBT ELECTRICAL SPECIFICATIONS (T _C = 25 °C unless otherwise noted)						
PARAMETER	SYMBOL	SYMBOL TEST CONDITIONS		TYP.	MAX.	UNITS
Collector to emitter breakdown voltage	V _{(BR)CES}	T _J = 25 °C	1200	-	-	
Collector to emitter voltage	V _{CE(on)}	$V_{GE} = 15 \text{ V}, I_{C} = 400 \text{ A}, T_{J} = 25 ^{\circ}\text{C}$	-	1.9	-	
Collector to enfitter voltage		V _{GE} = 15 V, I _C = 400 A, T _J = 125 °C	-	2.1	-	ľ
Gate to emitter threshold voltage	V _{GE(th)}	$V_{CE} = V_{GE}$, $I_{C} = 16$ mA, $T_{J} = 25$ °C	5.0	6.2	7.0	
Collector cut-off current	I _{CES}	$V_{CE} = V_{CES}$, $V_{GE} = 0$ V, $T_{J} = 25$ °C	-	-	5.0	mA
Gate to emitter leakage current	I _{GES}	$V_{GE} = V_{GES}$, $V_{CE} = 0$ V, $T_J = 25$ °C	-	=	400	nA

SWITCHING CHARACTERISTICS	3					
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Turn-on delay time	t _{d(on)}		-	125	-	
Rise time	t _r]	-	71	-	ns mJ
Turn-off delay time	t _{d(off)}	$V_{CC} = 600 \text{ V}, I_{C} = 400 \text{ A}, R_{g} = 2.5 \Omega,$	-	540	-	
Fall time	t _f	V _{GE} = ± 15 V, T _J = 25 °C	-	72	-	
Turn-on switching loss	E _{on}	7	-	44	-	
Turn-off switching loss	E _{off}	7	-	40	-	
Turn-on delay time	t _{d(on)}		-	130	-	ns ns
Rise time	t _r	7	-	75	-	
Turn-off delay time	t _{d(off)}	V_{CC} = 600 V, I_{C} = 400 A, R_{g} = 2.5 Ω , V_{GE} = ± 15 V, T_{J} = 125 °C	-	600	-	
Fall time	t _f		-	80	-	
Turn-on switching loss	E _{on}	7	-	48	-	1
Turn-off switching loss	E _{off}	1	-	43	-	- mJ
Input capacitance	C _{ies}		-	32.7	-	
Output capacitance	C _{oes}	V _{GE} = 0 V, V _{CE} = 25 V, f = 1.0 MHz	-	2.42	-	nF
Reverse transfer capacitance	C _{res}	7	-	1.50	-	
SC data	I _{SC}	$t_{sc} \le 10 \ \mu s, \ V_{GE} = 15 \ V, \ T_J = 125 \ ^{\circ}C, \ V_{CC} = 900 \ V, \ V_{CEM} \le 1200 \ V$	-	1900	-	Α
Internal gate resistance	R _g		-	2	-	Ω
Stray inductance	L _{CE}		-	-	18	nΗ
Module lead resistance, terminal to chip	R _{CC'+EE'}	T _C = 25 °C	-	0.32	-	mΩ

DIODE ELECTRICAL SPECIFICATIONS (T _C = 25 °C unless otherwise noted)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Diode forward voltage	V _F	I _F = 400 A	T _J = 25 °C	ı	2.15	2.20	V
blode lorward voltage			T _J = 125 °C	ı	1.99	2.03	
Diada waxaa waxaa ahaana	Q _{rr}	$\begin{array}{c} Q_{rr} \\ \\ I_{rr} \\ \\ E_{rec} \end{array} \qquad \begin{array}{c} I_F = 400 \text{ A, } V_R = 600 \text{ V,} \\ dI_F/dt = -6000 \text{ A/}\mu\text{s,} \\ V_{GE} = -15 \text{ V} \end{array}$	$T_J = 25 ^{\circ}C$	-	52	-	
Diode reverse recovery charge			T _J = 125 °C	-	64	-	μC
Diada pagk rayaraa ragayary gurrant			T _J = 25 °C	-	360	-	^
Diode peak reverse recovery current	¹rr		T _J = 125 °C	-	420	-	Α
Diede vevere veervan enever	F		T _J = 25 °C	-	20	=	mJ
Diode reverse recovery energy	∟rec		T _J = 125 °C	-	27	-	IIIJ



THERMAL AND MECHANICAL SPECIFICATIONS							
PARAMETER		SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Operating junction temperatu	re range	T_J		-40	-	150	• °C
Storage temperature range		T _{STG}		-40	-	125	
Junction to case,	IGBT	٥		-	-	0.048	
per 1/2 module	Diode	R_{thJC}		-	-	0.085	K/W
Case to sink		R _{thCS}	Conductive grease applied	-	0.032	=.	
Mounting torque			Power terminal screw: M6	2.5 to 5.0		Nm	
			Mounting screw: M6 3.0 to 6.0)	INIII	
Weight					340		g

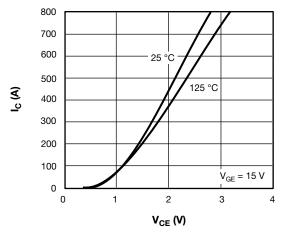


Fig. 1 - Typical Output Characteristics

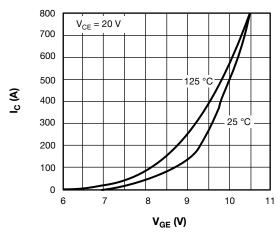


Fig. 2 - Typical Transfer Characteristics

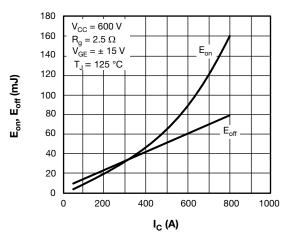


Fig. 3 - Switching Loss vs. I_C

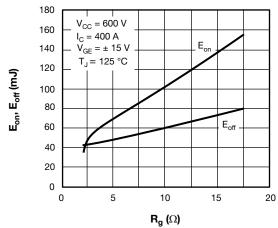


Fig. 4 - Switching Loss vs. R_a



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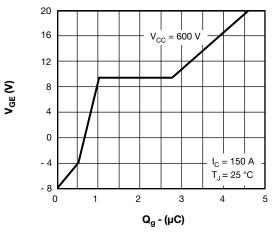


Fig. 5 - Gate Charge Characteristics

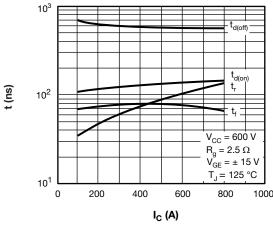


Fig. 7 - Typical Switching Time vs.I_C

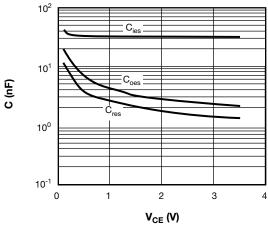


Fig. 6 - Typical Capacitance vs. Collector-to-Emitter Voltage

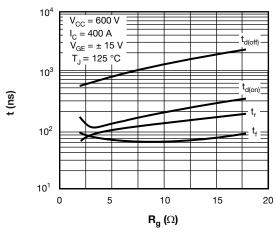


Fig. 8 - Typical Switching Time vs. Gate Resistance R_q

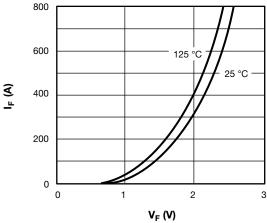


Fig. 9 - Typical Forward Characteristics Diode

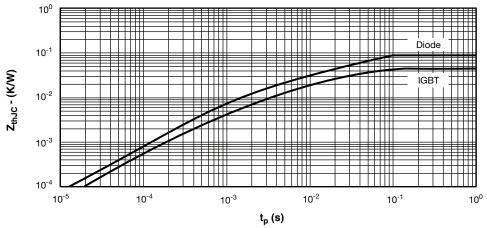
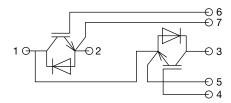


Fig. 10 - Transient Thermal Impedance

CIRCUIT CONFIGURATION

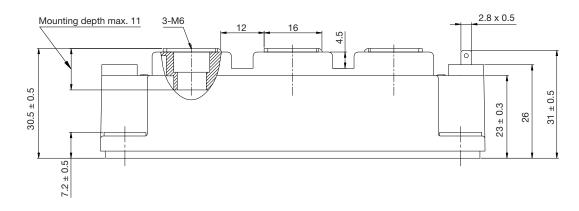


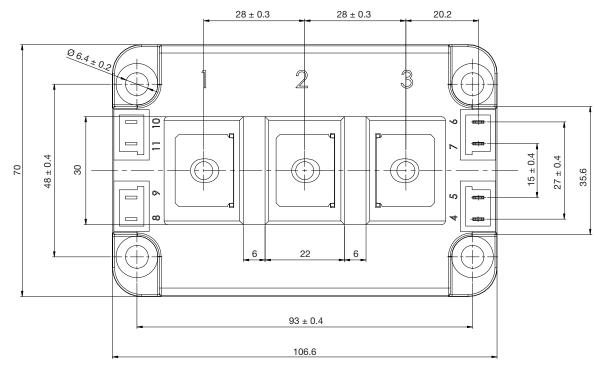
LINKS TO RELATED DOCUMENTS			
Dimensions	www.vishay.com/doc?95538		



Double INT-A-PAK

DIMENSIONS in millimeters (inches)







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